

Model Name: GA-P55-UD3L 1.0

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU LGA1156-A
05	CPU LGA1156-B
06	CPU LGA1156-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	DDR III POWER CAP
10	PCH FDI,DMI,USB,PCIE,NVRAM
11	PCH DP,CLK BUFFER
12	PCH HOST,SATA,PCI
13	PCH GPIO,CTRL,AUDIO
14	PCH PWR,GND
15	PCI EXPRESS*16 SLOT
16	PCI EXPRESS*4 SLOT
17	PCI EXPRESS*1 SLOT
18	PCI SLOT X4
19	ITE 8720 LPC IO
20	COM, -PROHOT , DYNAMIC OC , LPT
21	Dual BIOS
22	ALC888
23	REAR AUDIO JACK
24	CLOCK GEN ICS9LPRS914
25	VCORE PWM ISL6334CR
26	CPU VTT PWM ISL6322G
27	DDR 15V & VCC1 05 PCH PWM ISL6545CBZ

SHEET

TITLE

28	DISCRETE POWER
29	F PANEL ,USB , FDD
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31	J363
32	REALTEK RTL8111D
33	TPM SLB9635TT
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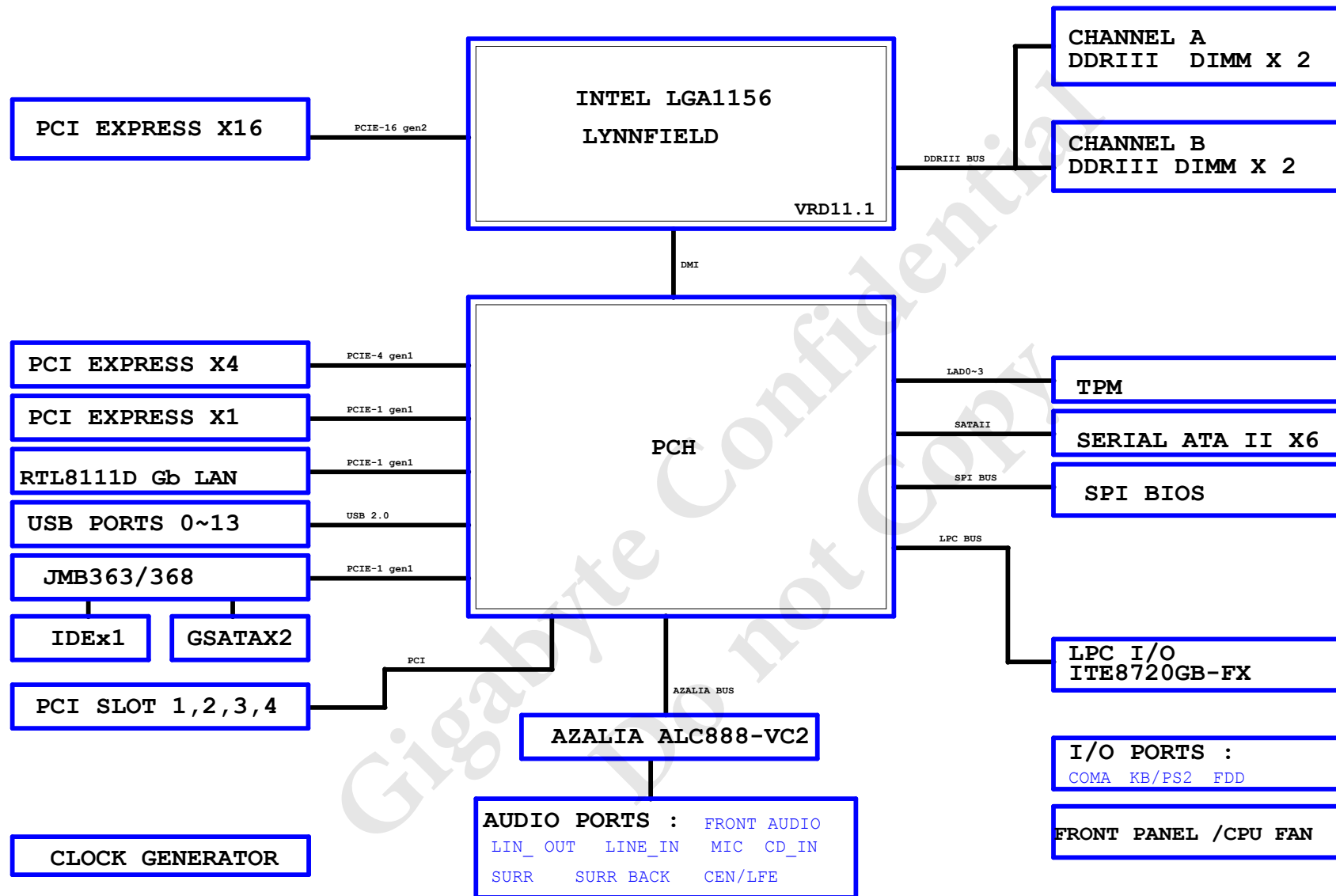
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BLOCK DIAGRAM



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Title	BLOCK DIAGRAM		
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LGA1156A

MAAA0	AW18	SA_MA[0]	AK3	DQSA0
MAAA1	AW15	SA_MA[1]	AK3	DQSA0
MAAA2	AW15	SA_MA[2]	AK3	DQSA0
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MAAA4	AW14	SA_MA[4]	AK3	DQSA0
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MAAA7	AW13	SA_MA[7]	AK3	DQSA0
MAAA8	AW14	SA_MA[8]	AK3	DQSA0
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MAAA10	AT19	SA_MA[10]	AK3	DQSA0
MAAA11	AU13	SA_MA[11]	AK3	DQSA0
MAAA12	AW11	SA_MA[12]	AK3	DQSA0
MAAA13	AU24	SA_MA[13]	AK3	DQSA0
MAAA14	AT11	SA_MA[14]	AK3	DQSA0
MAAA15	AR10	SA_MA[15]	AK3	DQSA0
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-SCASA	AT22	SA_CAS#	AK3	DQSA0
-SRASA	AT20	SA_RAS#	AK3	DQSA0
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SBA2	AV12	SA_BS[2]	AK3	DQSA0
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-CSA1	AW24	SA_CS[1]	AK3	DQSA0
-CSA2	AU21	SA_CS[2]	AK3	DQSA0
-CSA3	AU23	SA_CS[3]	AK3	DQSA0
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CKEA2	AV10	SA_CKE[2]	AK3	DQSA0
CKEA3	AY10	SA_CKE[3]	AK3	DQSA0
MODT_A0	AV23	SA_ODT[0]	AK3	DQSA0
MODT_A1	AV24	SA_ODT[1]	AK3	DQSA0
MODT_A2	AW23	SA_ODT[2]	AK3	DQSA0
MODT_A3	AY24	SA_ODT[3]	AK3	DQSA0
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-DCLKA0	AR21	SA_CLK[0]	AK3	DQSA0
-DCLKA1	AP18	SA_CLK[1]	AK3	DQSA0
-DCLKA1	AN18	SA_CLK[1]	AK3	DQSA0
-DCLKA2	AN21	SA_CLK[2]	AK3	DQSA0
-DCLKA2	AP21	SA_CLK[2]	AK3	DQSA0
-DCLKA3	AP19	SA_CLK[3]	AK3	DQSA0
-DCLKA3	AN19	SA_CLK[3]	AK3	DQSA0
-DDR3_RST	AV8	SM_DRAMRST#	AK3	DQSA0
TP1	AK22	SA_CS#	AK3	DQSA0
TP10	AK22	SA_CS#	AK3	DQSA0
TP19	AL23	SA_CS#	AK3	DQSA0
TP19	AK23	SA_CS#	AK3	DQSA0
DQSA8	AL10	SA_DQS#	AK3	DQSA0
-DQSA8	AM10	SA_DQS#	AK3	DQSA0
SACB0	AP10	SA_ECC_CB[0]	AK3	DQSA0
SACB1	AN10	SA_ECC_CB[1]	AK3	DQSA0
SACB2	AR11	SA_ECC_CB[2]	AK3	DQSA0
SACB3	AP11	SA_ECC_CB[3]	AK3	DQSA0
SACB4	AK9	SA_ECC_CB[4]	AK3	DQSA0
SACB5	AL9	SA_ECC_CB[5]	AK3	DQSA0
SACB6	AK11	SA_ECC_CB[6]	AK3	DQSA0
SACB7	AM11	SA_ECC_CB[7]	AK3	DQSA0

DDR_A

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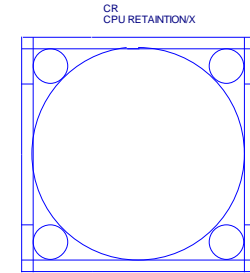
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LGA1156B

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MAAB7	AT17	SB_MA[7]	AD4	MD83
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MAAB9	AY16	SB_MA[9]	AD2	MD85
MAAB10	AV15	SB_MA[10]	AD1	MD86
MAAB11	AW16	SB_MA[11]	AD0	MD87
MAAB12	AW15	SB_MA[12]	AH6	DQSB1
MAAB13	AY12	SB_MA[13]	AH5	DQSB1
MAAB14	AY11	SB_MA[14]	AH4	DQSB1
MAAB15	AV11	SB_MA[15]	AH3	DQSB1
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-SCASB	SCASB	SB_CAS#	AG4	MD89
-SRASB	SRASB	SB_RAS#	AG3	MD90
SBAB0	AV25	SB_BS[0]	AG2	MD91
SBAB1	AV25	SB_BS[1]	AG1	MD92
SBAB2	AV12	SB_BS[2]	AG0	MD93
-CSB0	CSB0	SB_CS[0]	AG6	MD94
-CSB1	CSB1	SB_CS[1]	AG5	MD95
-CSB2	CSB2	SB_CS[2]	AG4	MD96
-CSB3	CSB3	SB_CS[3]	AG3	MD97
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CKEB3	CKEB3	SB_CKE[3]	AG6	MD101
MODT_B0	AU27	SB_ODT[0]	AG5	MD102
MODT_B1	AU29	SB_ODT[1]	AG4	MD103
MODT_B2	AV27	SB_ODT[2]	AG3	MD104
MODT_B3	AU28	SB_ODT[3]	AG2	MD105
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SBCB2	AN15	SB_ECC_CB[2]	AG4	MD110
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SBCB4	AM12	SB_ECC_CB[4]	AG2	MD112
SBCB5	AN12	SB_ECC_CB[5]	AG1	MD113
SBCB6	AN14	SB_ECC_CB[6]	AG0	MD114
SBCB7	AP13	SB_ECC_CB[7]	AG6	MD115
TP12	AK23	SB_CS#	AG5	MD116
TP13	AK24	SB_CS#	AG4	MD117
TP15	AK24	SB_CS#	AG3	MD118
TP17	AK24	SB_CS#	AG2	MD119
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-DCLKB1	AR15	SB_CLK[1]	AG5	MD123
-DCLKB2	AR16	SB_CLK[2]	AG4	MD124
-DCLKB2	AR16	SB_CLK[2]	AG3	MD125
-DCLKB3	AR13	SB_CLK[3]	AG2	MD126
-DCLKB3	AR13	SB_CLK[3]	AG1	MD127
TP12	AK23	SB_CS#	AG0	MD128
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SBCB4	AM12	SB_ECC_CB[4]	AG4	MD138
SBCB5	AN12	SB_ECC_CB[5]	AG3	MD139
SBCB6	AN14	SB_ECC_CB[6]	AG2	MD140
SBCB7	AP13	SB_ECC_CB[7]	AG1	MD141
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SBCB4	AM12	SB_ECC_CB[4]	AG4	MD152
SBCB5	AN12	SB_ECC_CB[5]	AG3	MD153
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SBCB7	AP13	SB_ECC_CB[7]	AG1	MD155
TP12	AK23	SB_CS#	AG0	MD156
TP13	AK24	SB_CS#	AG6	MD157
TP15	AK24	SB_CS#	AG5	MD158
TP17	AK24	SB_CS#	AG4	MD159
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SBCB4	AM12	SB_ECC_CB[4]	AG4	MD166
SBCB5	AN12	SB_ECC_CB[5]	AG3	MD167
SBCB6	AN14	SB_ECC_CB[6]	AG2	MD168
SBCB7	AP13	SB_ECC_CB[7]	AG1	MD169

DDR_B
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LGA1156[10SC1-F01156-01R]



Need check the new CPU ME

LGA1156

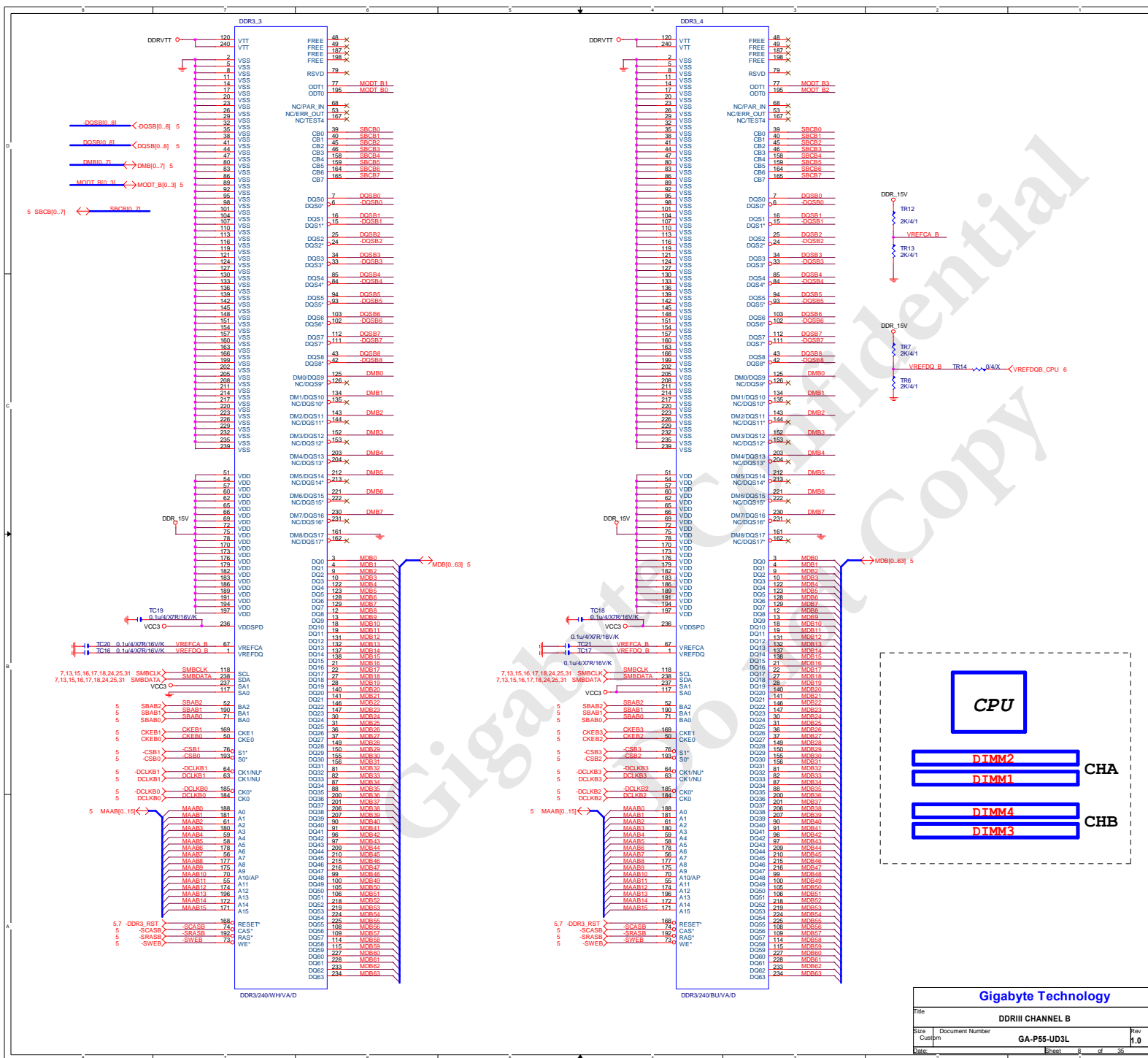


PLATE+LM[12KRC-0F0001-01R]

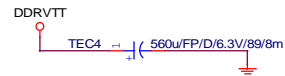
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CPU LGA1156-B

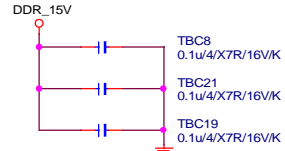
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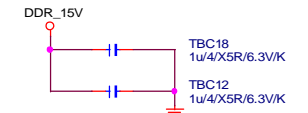
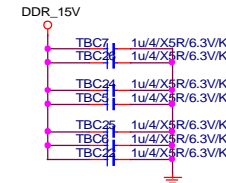
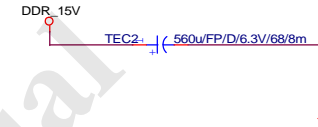
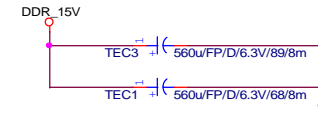
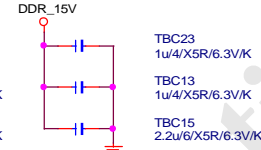
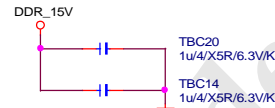
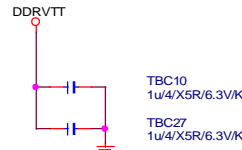
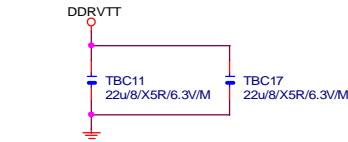
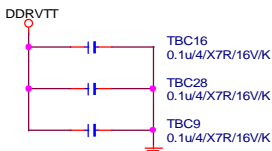
DDR TERMINATION CHANNEL A/B



DDR15V Decouple



DDRVTT Decouple

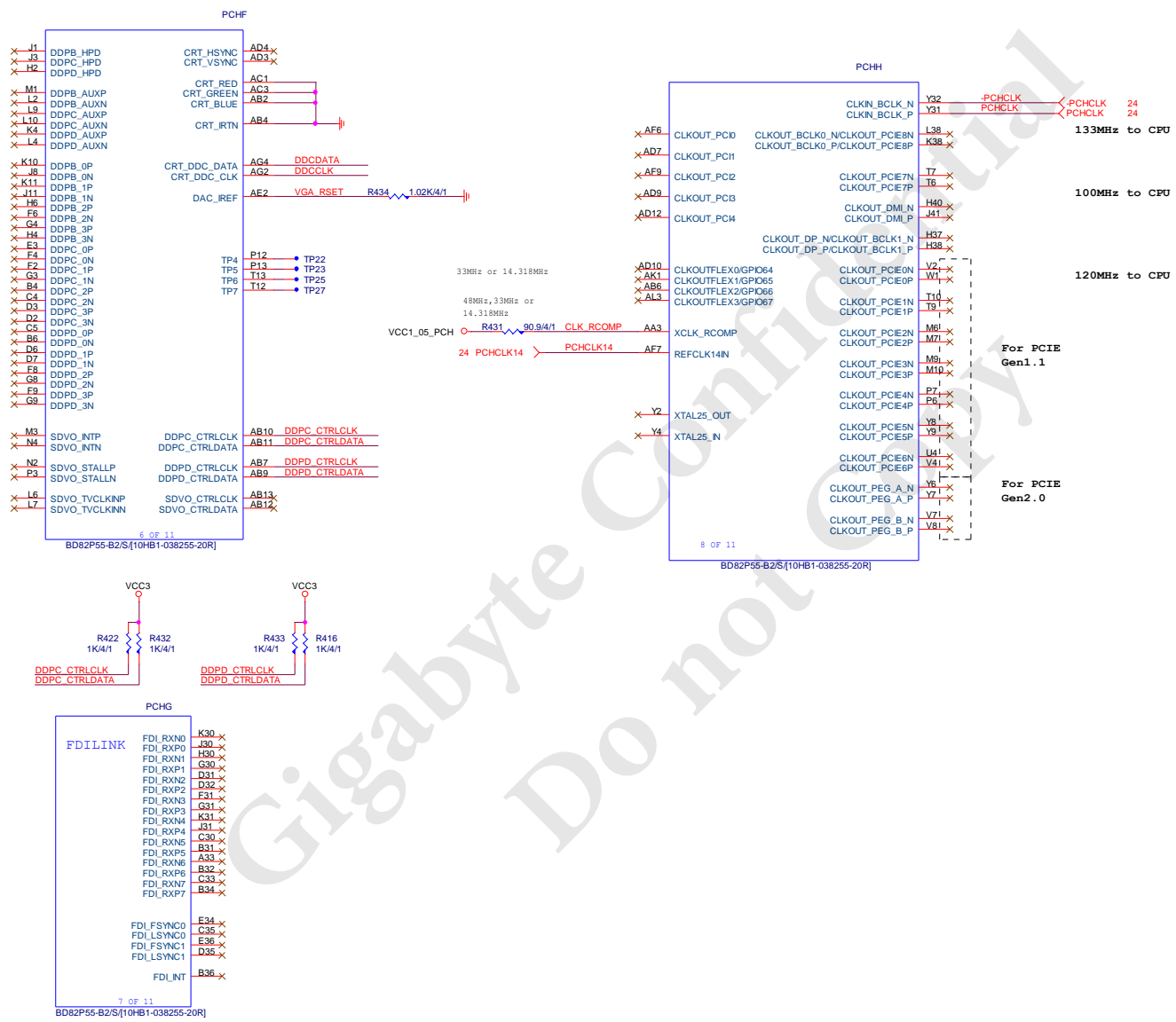


COUPON1 COUPON1 1 2 COUPONX VCC3

COUPON2 COUPON2 1 2 COUPONX

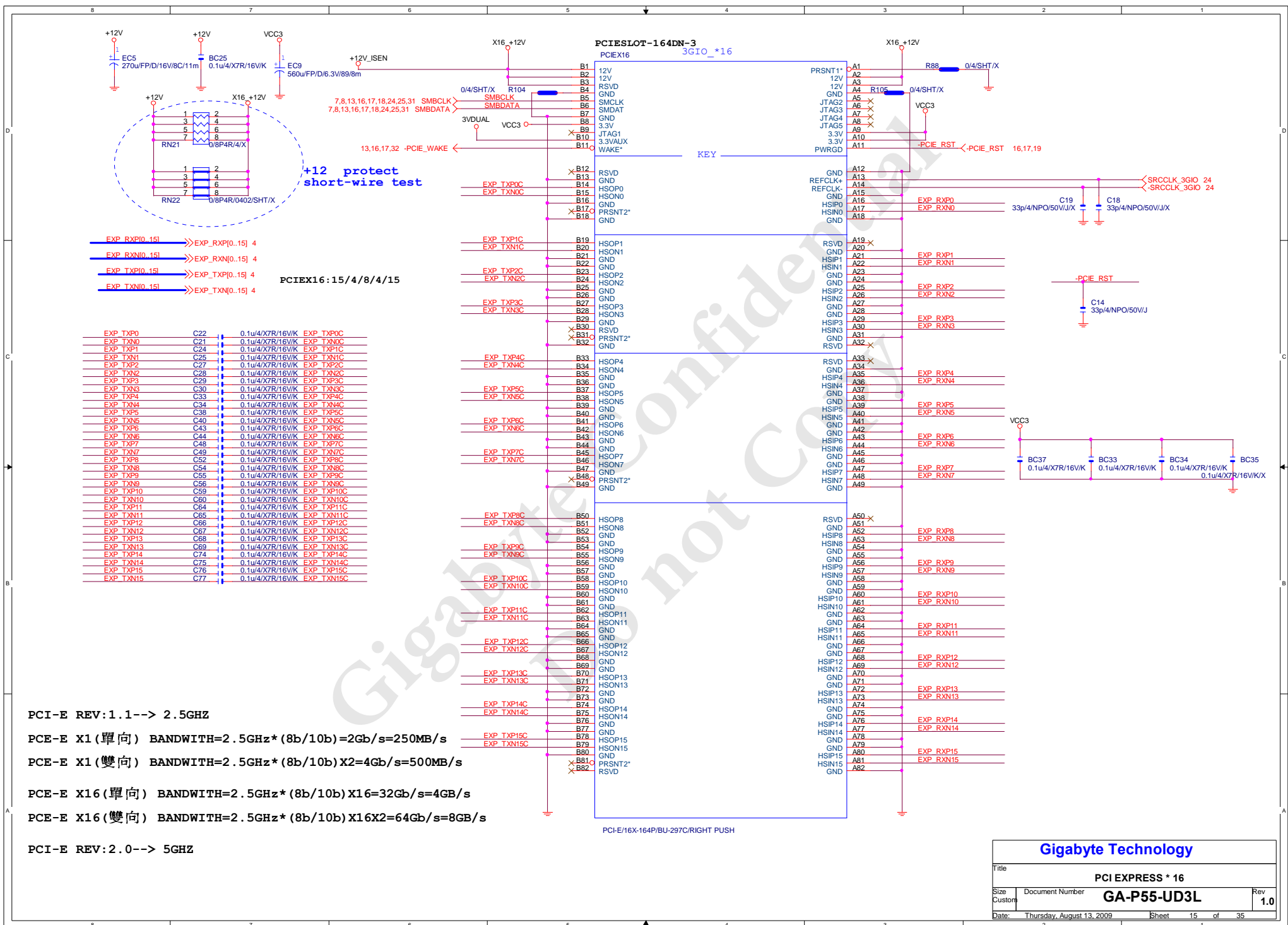
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DDRIII POWER CAP			
Size B	Document Number	GA-P55-UD3L	Rev 1.0
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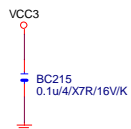


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Title			
PCH DISPLAY ,CLK BUFFER			
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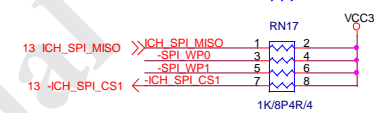
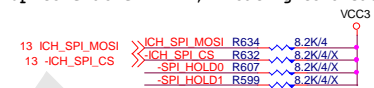
r1.0 DG;0.7 CRB

BOOT DEVICE	GNT1	GNT0
LPC	0	0
PCI	0	1
SPI	1	1

1 means floating
0 means PD 1K

IC8SO-SOCKET

MOSI pull up to enable iTPM , floating to disable



Default int pull up



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Title		BIOS	
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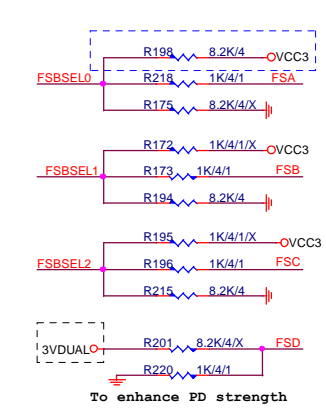


CLK GEN CK505

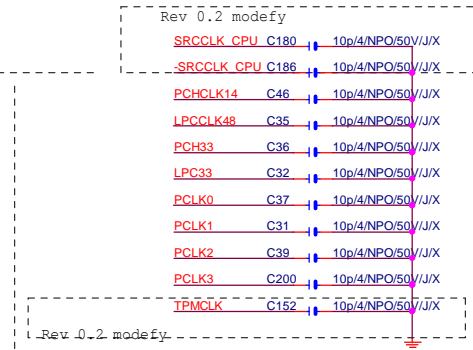
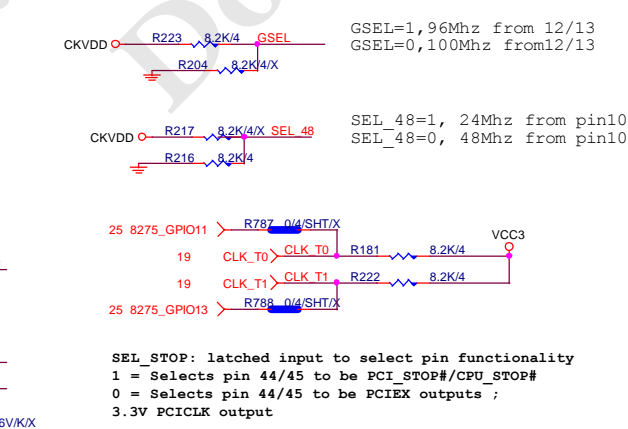
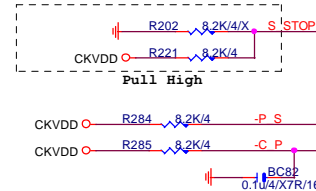
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FSC	FSB	FSA	CPU
0	0	0	266MHz
0	0	1	133MHz
0	1	0	200MHz
0	1	1	166MHz
1	0	0	333MHz
1	1	0	400MHz



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Title: CK505 CLK GEN

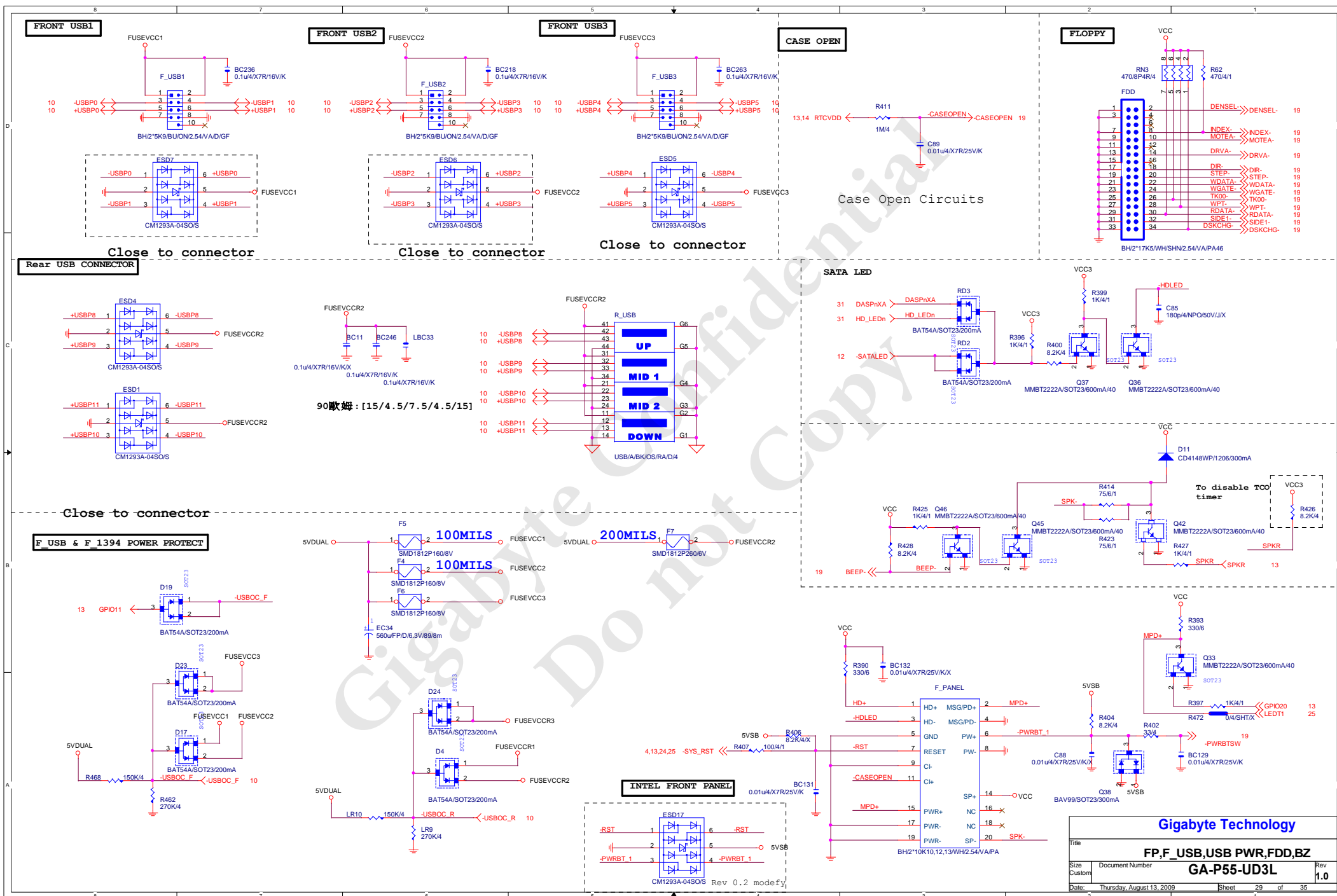
GA-P55-UD3L

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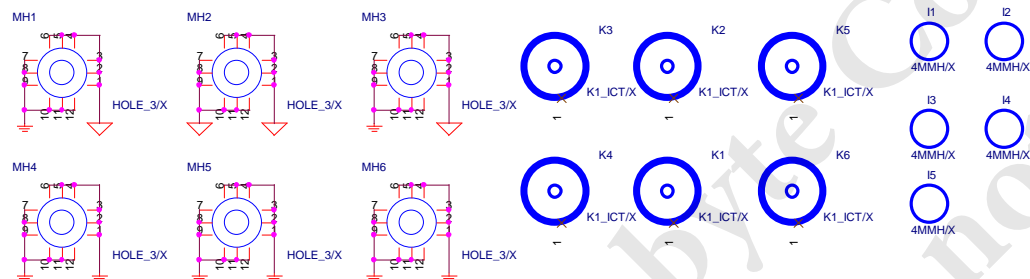
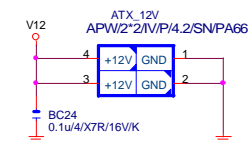




ATX POWER CONNECTOR

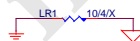
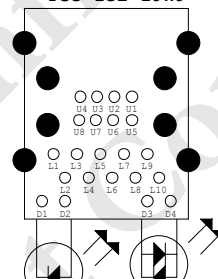
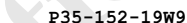
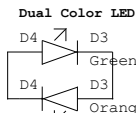
Diagram illustrating the ATX Power Connector circuit, showing the connection of the ATX power plug to the motherboard's power pins. The diagram includes various components and labels:

- ATX Connector Pins:** 13 (3.3V), 14 (-12V), 15 (GND), 16 (PSON), 17 (GND), 18 (GND), 19 (GND), 20 (-5V), 21 (5V), 22 (5V), 23 (5V), 24 (GND).
- Power Rails:** -12V, VCC3, 3.3V, -5V, 5V, 5VSB, 12V, GND.
- Capacitors:** BC231 (0.1u/4/X7R/16V/K), BC228 (1u/6/X7R/16V/K), BC232 (0.1u/4/X7R/16V/K/X), BC238 (0.1u/4/X7R/16V/K/X), BC225 (1u/4/X5R/6.3V/K), BC235 (1u/4/X5R/6.3V/K), BC227 (510/6), BC229 (1u/6/X7R/16V/K), BC230 (510/6), BC233 (0.1u/4/X7R/16V/K), BC234 (0.1u/4/X7R/16V/K), BC24 (0.1u/4/X7R/16V/K).
- Other Components:** APW/2*12IV/A/SN2SHKPA66, V12, BC21 (0.1u/4/X7R/16V/K).
- Notes:** To prevent the 5VSB under loading when boot.
- ATX 12V Connector:** APW/2*12IV/P/4.2/SNPA66, V12, BC24 (0.1u/4/X7R/16V/K).
- Other Labels:** MH1, MH2, MH3, MH4, MH5, MH6, HOLE_3/X, K1, K2, K3, K4, K5, K6, I1, I2, I3, I4, I5, 4MMH/X.

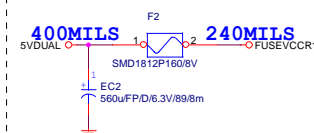
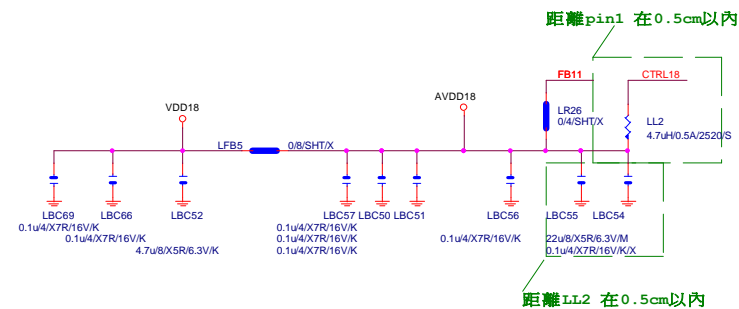
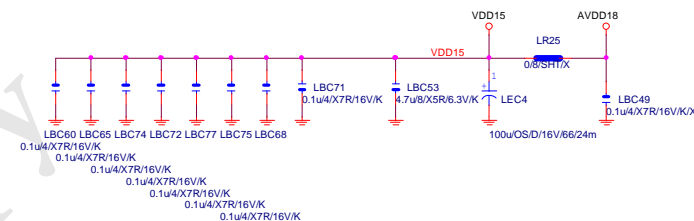
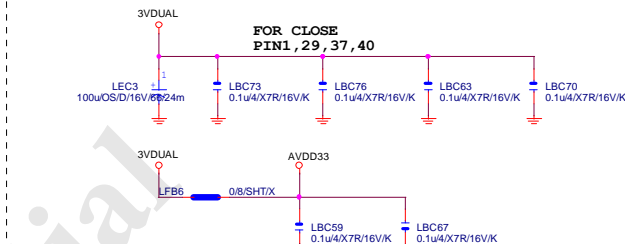
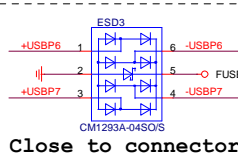
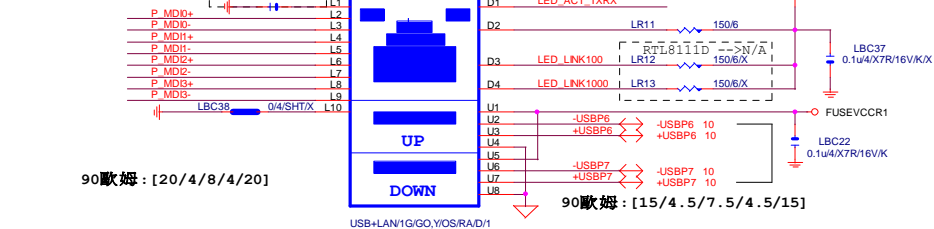


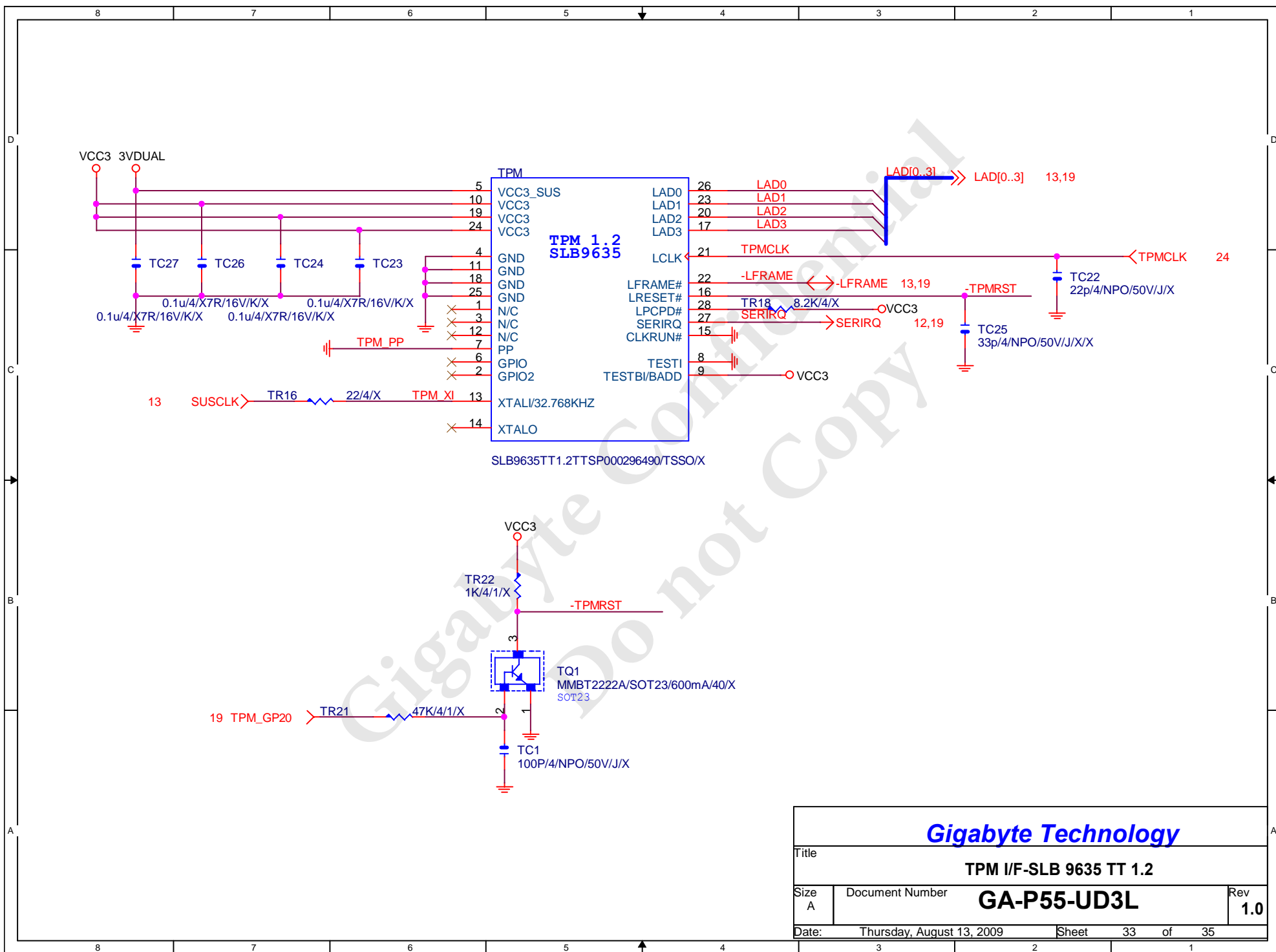
Power domain chart

```
for RT8111B N/C
for RT8111C 0 ohm
  (Internal Regulator)
for RT8111C N/C
  (external Regulator)
```

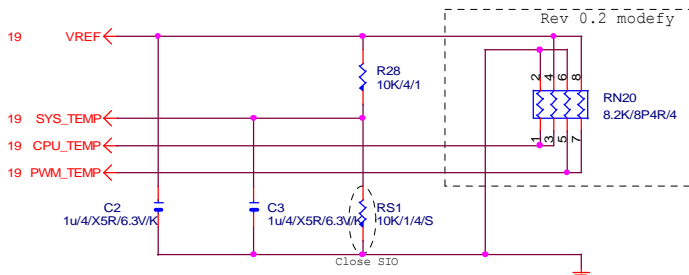


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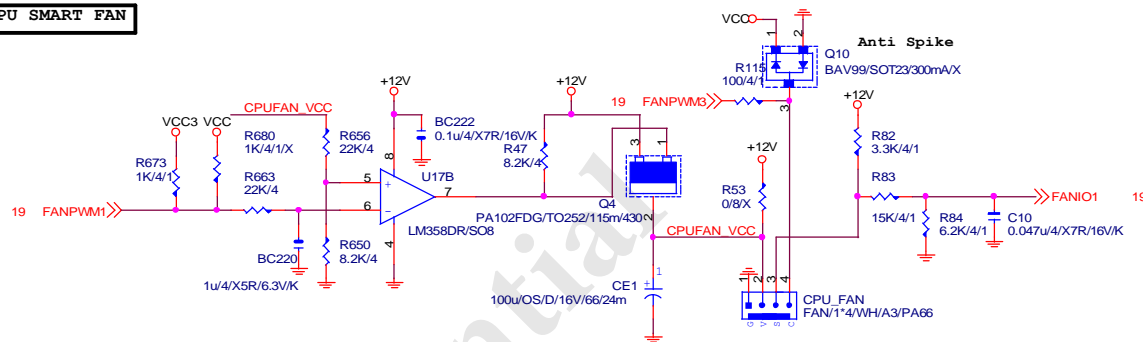




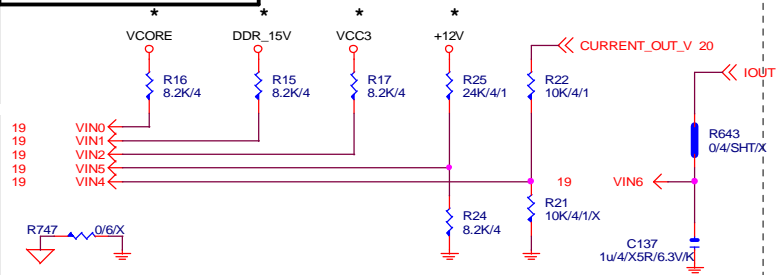
TEMP H/W MONITOR



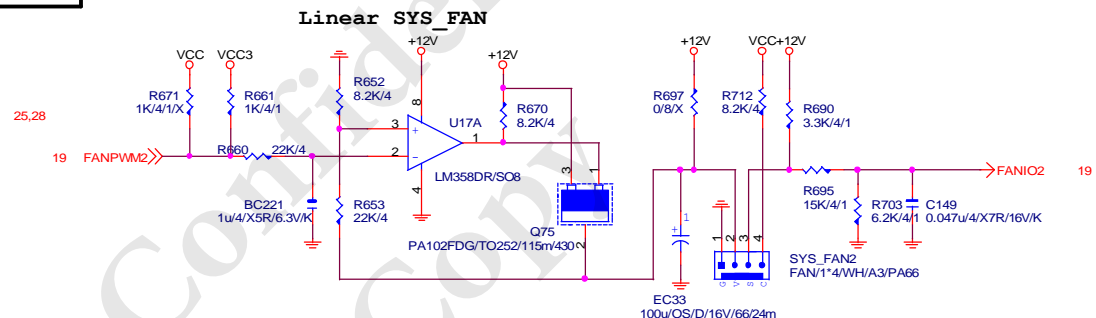
CPU SMART FAN



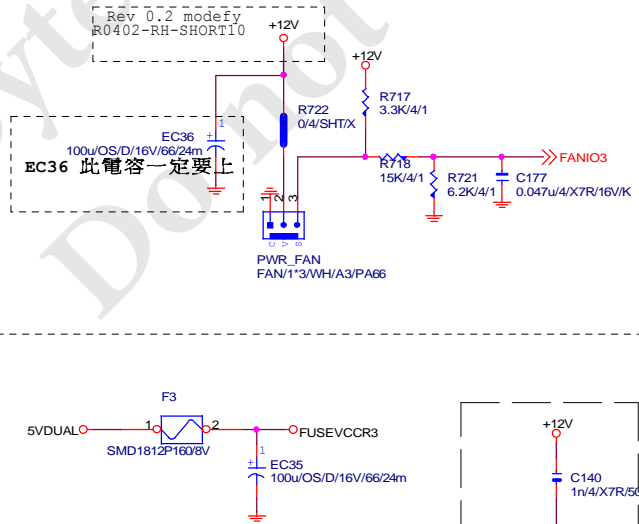
VOLTAGE-- H/W MONITOR



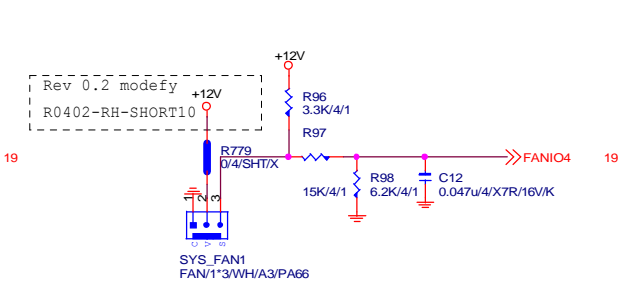
SYS FAN2



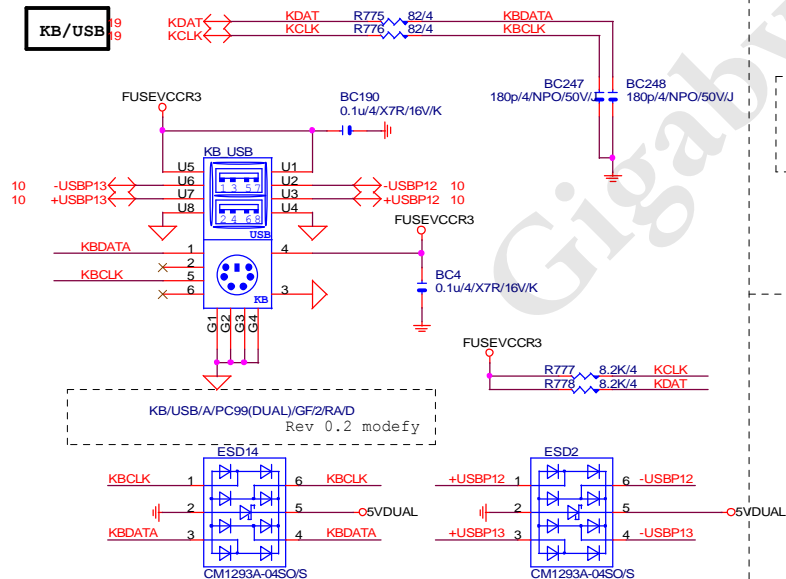
PWR FAN



SYS FAN1



KB/USB



Gigabyte Technology

Title			HWM,KB/MS, FAN CTRL
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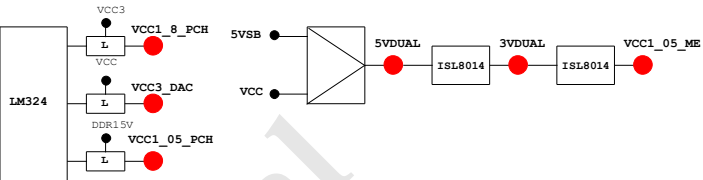
PCH GPIO LIST TABLE

PIN NAME	PWR	Default	USAG	NOTE
GP0	MAIN	H-Z	GPI -PECI_REQ	N/A
GP1/TACH1	MAIN		GPI ICH_FAN_TACH1	N/A
GP2/PIRQE#	MAIN		GPI -PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN		GPI -PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN		GPI -PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN		GPI -PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN		GPI ICH_FAN_TACH2	N/A
GP7/TACH3	MAIN		GPI ICH_FAN_TACH3	N/A
GP8	STBY	H	GPO GPIO8	P/U 8.2K 3VDUAL
GP9/OC5#	STBY		NATIVE OC5#	N/A
GP10/OC6#	STBY		NATIVE OC6#	N/A
GP11/SMBALERT#	STBY		NATIVE -SMBALERT	P/U 8.2K 3VDUAL
GP12	STBY	L	GPI LAN_PHY_PWR_CTRL	P/U 8.2K 3VDUAL
GP13	STBY	L	GPI GPIO13	P/U 8.2K 3VDUAL
GP14/OC7#	STBY		NATIVE OC7#	N/A
GP15	STBY	L	GPO GPIO15	N/A
GP16	MAIN		GPI -SKTOCC	P/U 8.2K VCC3
GP17/TACH0	MAIN		GPI ICH_FAN_TACH0	N/A
GP18	MAIN		NATIVE MB_ID0	P/D 8.2K GND
GP19	MAIN		GPI -LAN1_ISO	P/U 8.2K VCC3
GP20	MAIN		NATIVE LED_CTL	P/U 1K VCC3
GP21	MAIN		GPI VCC18_PCH_OV2	P/U 8.2K VCC3
GP22	MAIN	H-Z	GPI VCORE_OV3	P/U 8.2K VCC3
GP23	MAIN		NATIVE -LDRQ1	P/U 8.2K VCC3
GP24	STBY	L	GPO TLS	P/U 8.2K 3VDUAL
GP25	STBY		NATIVE -CPU_STOP	P/U 8.2K 3VDUAL
GP26	STBY		NATIVE -ACZ_DET	P/U 8.2K 3VDUAL
GP27	STBY	H	GPO GPIO27	P/U 8.2K 3VDUAL
GP28	STBY	H	GPO GPIO28	P/U 8.2K 3VDUAL
GP29	STBY	L	GPI GPIO29	N/A
GP30	STBY	H-Z	GPI S_PWR_ACK	P/U 100K 3VDUAL
GP31	STBY	H-Z	GPI N/A(Reverse)	P/U 8.2K VCC3
GP32	MAIN	H	GPO MB_ID1	P/D 8.2K GND
GP33	MAIN	H	GPO LOAD-1LINE	P/U 1K VCC3
GP34	MAIN	H-Z	GPI -PCI_STOP	P/U 8.2K VCC3
GP35	MAIN	L	GPO GPIO35	P/U 8.2K VCC3
GP36	MAIN		GPI -LAN1_DSM	P/U 8.2K VCC3
GP37	MAIN		GPI N/A	P/U 8.2K VCC3
GP38	MAIN	H-Z	GPI VCORE_OV2	P/U 8.2K VCC3
GP39	MAIN	H-Z	GPI -LAN_DSM	P/U 8.2K VCC3
GP40	STBY		NATIVE OCL#	N/A
GP41	STBY		NATIVE OC2#	N/A
GP42	STBY		NATIVE OC3#	N/A
GP43	STBY		NATIVE OC4#	N/A
GP44	STBY	L	NATIVE N/A	P/U 8.2K 3VDUAL
GP45	STBY		NATIVE -LPCPME	P/U 8.2K 3VDUAL
GP46	STBY	L	NATIVE PWR_LED	P/U 8.2K 3VDUAL
GP47	STBY		NATIVE PSI_LED	P/U 8.2K 3VDUAL
GP48	MAIN	H-Z	IN EN_PWM	P/U 8.2K VCC3
GP49	MAIN	H-Z	IN VCC18_OV1	P/U 8.2K VCC3
GP50	MAIN		NATIVE -REQ1	P/U 2.2K VCC
GP51	MAIN	H	NATIVE -GNT1	N/A
GP52	MAIN		NATIVE -REQ2	P/U 2.2K VCC
GP53	MAIN	H	NATIVE -GNT2	N/A
GP54	MAIN		NATIVE -REQ3	P/U 2.2K VCC
GP55	MAIN	H	NATIVE -GNT3	N/A
GP56	STBY		NATIVE N/A(Reverse)	P/U 8.2K 3VDUAL
GP57	STBY	H-Z	IN VCORE_OV1	P/U 8.2K 3VDUAL
GP58	STBY	H-Z	NATIVE F_USB_OC	P/U 8.2K 3VDUAL
GP59	STBY		NATIVE USB_OCO#	N/A
GP60	STBY	H-Z	NATIVE N/A(Reverse)	P/U 8.2K 3VDUAL
GP61	STBY	L	NATIVE -SUSTAT	N/A
GP62	STBY	L	NATIVE SUSCLK	N/A
GP63	STBY	L	NATIVE GPIO63	N/A
GP64	MAIN	L	NATIVE CLKOUTFLEX0	N/A
GP65	MAIN	L	NATIVE CLKOUTFLEX1	N/A
GP66	MAIN	L	NATIVE CLKOUTFLEX2	N/A
GP67	MAIN	L	NATIVE CLKOUTFLEX3	N/A
GP72	STBY	H-Z	NATIVE VCORE_OV4	P/U 8.2K 3VDUAL
GP73	STBY		NATIVE 1_05V_OV1	P/U 8.2K 3VDUAL
GP74	STBY	H-Z	NATIVE 1_05V_OV2	P/U 8.2K 3VDUAL
GP75	STBY	H-Z	NATIVE N/A(Reverse)	P/U 8.2K 3VDUAL

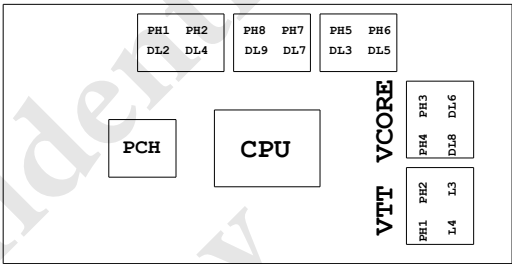
Super I/O ITE8720 GPIO Table

PIN NAME	USAG	NOTE
SVC/PECI_RQT/GP14	-PECI_REQ	
PWROK1/GP13	PWROK1/ITE_PWROK	
KRST#/GP62	-KBRST	
SO/GP50	-ICH_SPI_CS	
IRTX/GP47/CE2_N/JP7	CEB_N	
GP46/IRRX	-LAN2_DSM	
PSION#/GP42	-PSON	
PWROK2#/GP41	PECI_CTL	
PCIRST3#/GP10/VDIMM_STR_EN	-PCIE_RST	
RSMRST#CIRRK1/GP55	-RSMRST	
PME#/GP54	-LPCPME	
PD5/GP75/BUSSO0	N/A	

PIN NAME	USAG	NOTE
FAN_TAC2/GP52	FANIO2	
FAN_TAC3/GP37	FANIO3	
VIDO3/FAN_TAC4/GP25/DSR2#	FANIO4	
FAN_CTL2/GP51	FANPWM2	
FAN_CTL3/GP36	FANPWM3	
VID4/GP34	BEEP-	
VID3/GP33	TURBO1	
VID2/GP32	TURBO0	
VCORE_GOOD/VID6/GP63	CPUT_LED1_C	
VID5/GP35	CPUT_LED2_C	
VID1/GP31	CPUT_LED3_C	
VID0/GP30	-LAN1_DSM	NBT_LED1_C
SLCT/GP80	CPU_LED1_C	
PE/GP81	CPU_LED2_C	
BUSY/GP82	CPU_LED3_C	
PD3/GP73/BUSSI1	SB_LED1_C	
PD4/GP74/BUSSI2	SB_LED2_C	
VCORE_EN/VID7/GP64	IT_GP64	SB_LED3_C
PD0/GP70	NB_LED1_C	
PD1/GP71	NB_LED2_C	
PD2/GP72/BUSSIO	NB_LED3_C	
GP22/SCK	LOW_PWR_1	
VID05/GP27/SIN2	LOW_PWR_2	
PCIRST2#/GP11	-PFMRST1	
PCIRST1#/GP12	-PFMRST2	
3VSB5W#/GP40	CSI_F0	BSEL166_1
SUSC#/GP53	CSI_F1	BSEL166_2
GP23/SI	BSEL166_3/CSISBSL	
VID00/GP20/CTS2#	CPUT_LED1_C	BSEL166_4
GP65/VDDA_EN/GB_01	MB_ID2	
PD6/GP76/BUSSO1	MB_ID3	
PD7/GP77/BUSSO2	MB_ID4	
AFD#/GP86/SMB_C_R	SEC_PIN	FST_2X8
INIT#/GP85/SMBD_M	SEC_2x8	GTLREF_AD2
ACK#/GP83	DDR_LED1_C	
VID01/GP21/DCD2#	DDR_LED2_C	
STB#/GP87/SMB_C_M	DDR_LED3_C	
PWRON#/GP44	VCORE_OV1	
PANSWH#/GP43	PWRBT5W	
KDAT/GP61	-PWRBT5W	
KCLK/GP60	KDAT	
MDAT/GP57	KCLK	
MACL/GP56	MDAT	
GP66/VLDT_EN/GB_02	NBT_LED1_C	MCLK
SVD/PCIRSTIN#/CIRRX/GP15	PWM2_CR	
KDAT/GP61	PWM2_CR	
GP67/CPU_PG/GB_03	EN_LOADLINE	IT_GP67/-EN_PWM2
SLIN#/GP84/SMBD_R	-EN_PWM2	
PSI_L/FAN_CLT5/CIRRX2/GP16	-THERM	
VID04/GP26/SOUT2	DDR18V_PH2_EN	
VID02/FAN_TAC5/GP24/DSR2#	DDR18V_LED	
VID06/GP17/RI2#	1_1V_PH_EN	
VID07/JP6/DTR2#	JP6	
PD5/GP75/BUSSO0	SB_LED3_C	



PWM各相位的擺法如下：



BIOS超電壓對應表：

線路圖名稱	BIOS選項
Vcore	CPU Vcore
CPU_VTT	CPU Termination
CPU_VAXG	CPU Graphic Core
VCC1_8_PCH	CPU PLL
VCC1_05_PCH	PCH core
3VDUAL	3VDUAL
DDR15V	DRAM voltage
DDRVTT	DRAM Termination
VREF_CA_A/VREF_CA_B	DRAM Address Ref
VREF_DQ_A/VREF_DQ_B	DRAM Data Ref

散熱模組料號：

8IBP:
1.12SP2-01A001-Y1R/Y2R
2.12SP2-01A001-Z1R/Z2R
(HIBRID模組)包材階

	3 pin FAN control	4 pin FAN control	FAN speed	Controller
CPU FAN	FANPWM1 ICH_FAN_PWM2	FANPWM3 ICH_FAN_PWM0	FANIO1 ICH_FAN_TACH0	IT8720 PCH
SYS FAN	FANPWM2 ICH_FAN_PWM1	N/A	FANIO2 ICH_FAN_TACH1	IT8720 PCH
PWR FAN	N/A	N/A	FANIO3 ICH_FAN_TACH2	IT8720 PCH

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TABLE LIST			
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